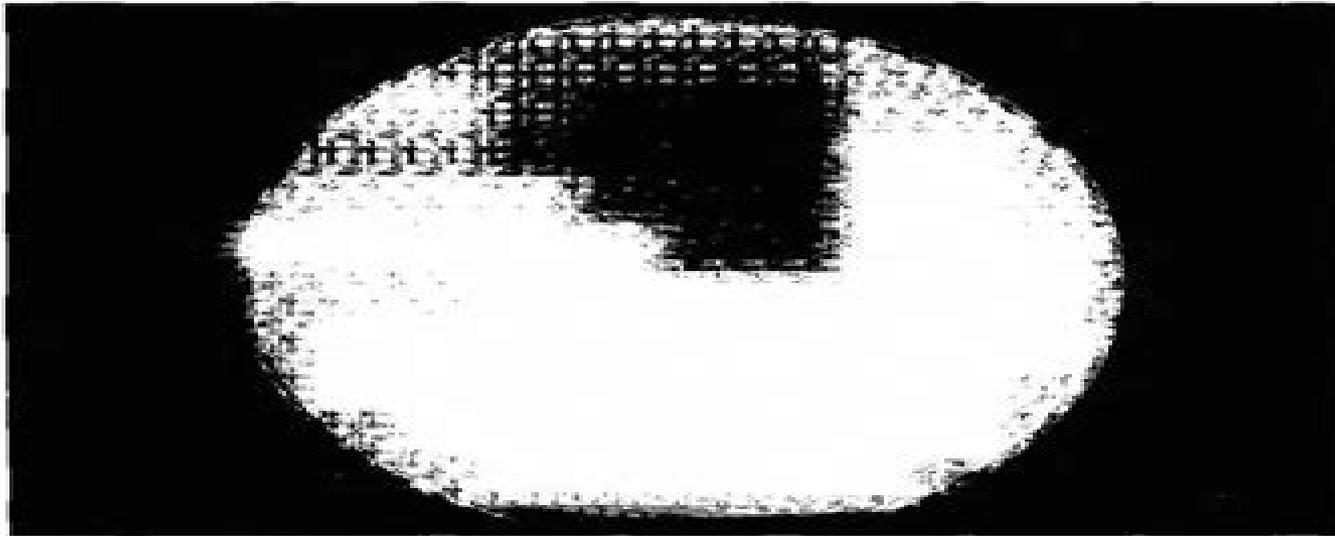


(a)



(b)

Figure 1.20 | (a) Model of a crystal puller and (b) photograph of a silicon wafer with an array of integrated circuits. The circuits are tested on the wafer then sawed apart into chips that are mounted into packages. (Photo courtesy of Intel Corporation.)

Ultrapurification Of Semiconductor Mater

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